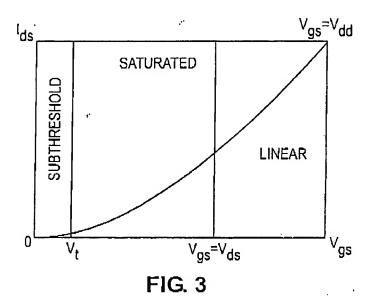


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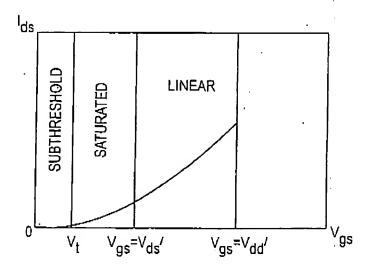


FIG. 4

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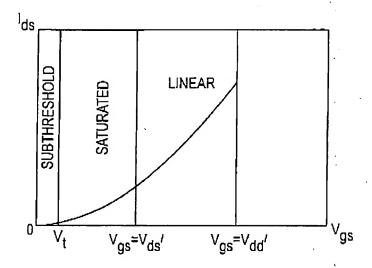
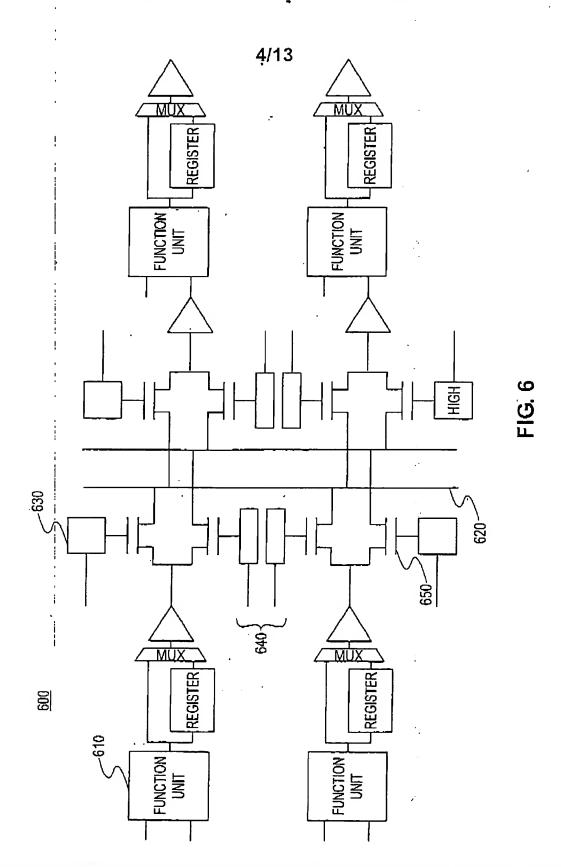


FIG. 5



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CONTROLLED BUFFER

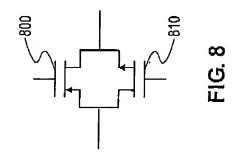
CONTROL

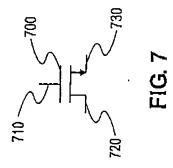
Solve 10

DATA OUT

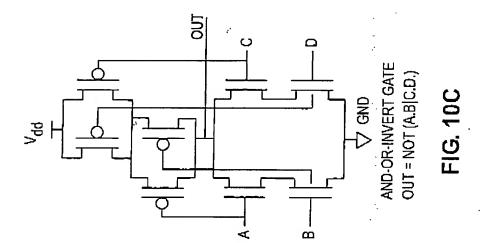
FIG. 9

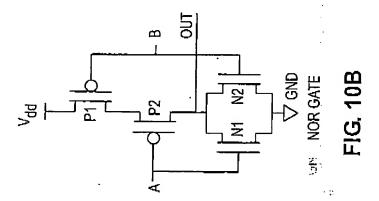
FIG. 9

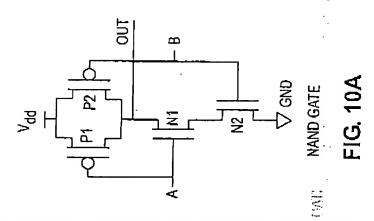


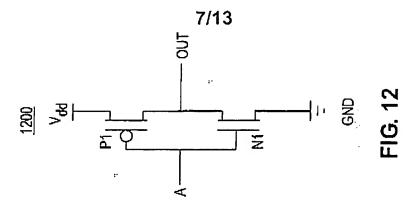


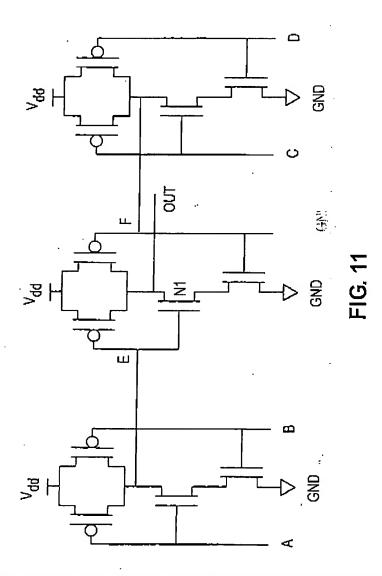
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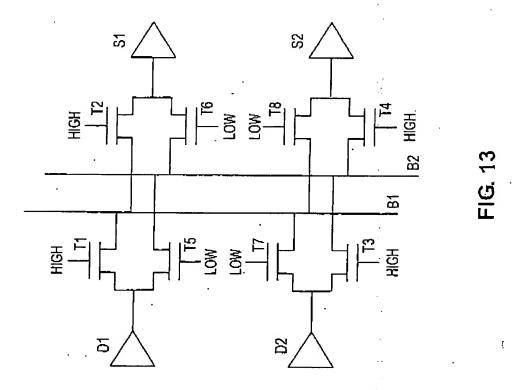


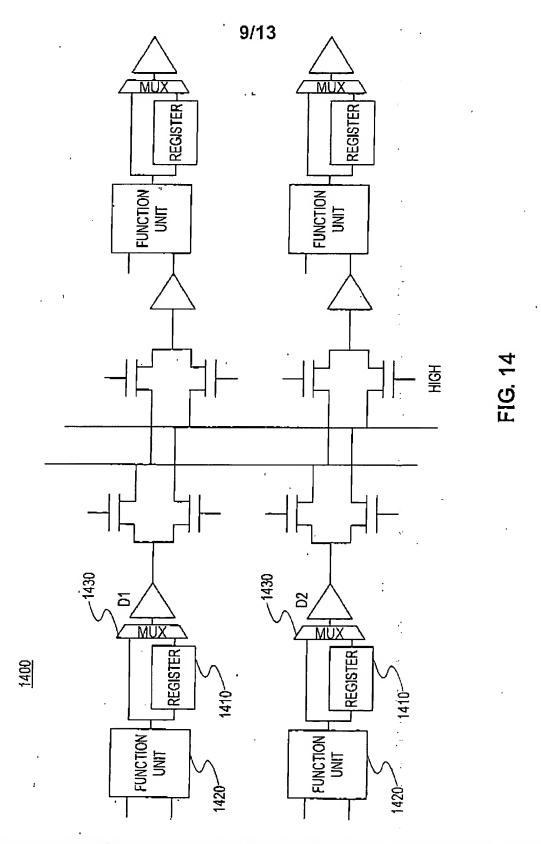






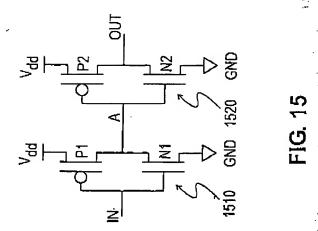
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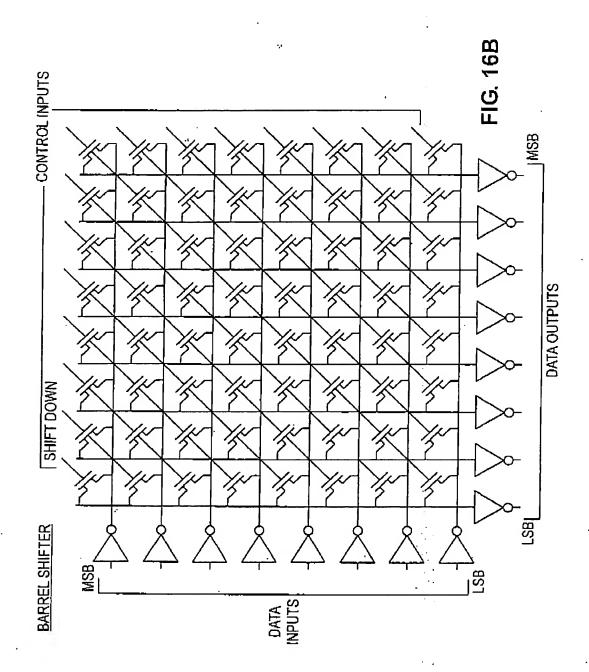


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10/13 16/10 16/20 ATA OUT
CONTROL



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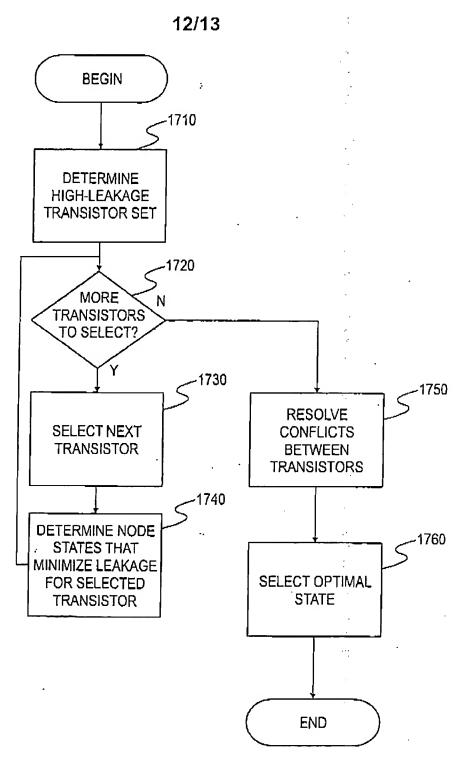


FIG. 17

